

APR-28-2004 15:05

HEWLETT PACKARD LGL FOLL

ALL RECEIVED  
CENTRAL FAX CENTER

970 898 7247

P.03

APR 28 2004

**HEWLETT-PACKARD COMPANY**  
Intellectual Property Administration  
P.O. Box 272400  
Mail Stop 35  
Fort Collins, Colorado 80527-2400

**PATENT APPLICATION**

**ATTORNEY DOCKET NO. 10007099-1**

**IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE**

# OFFICIAL

**Inventor(s):** Blaine D. Galtner & Russ W. Herrell

**Confirmation No. 2685**

**Serial No.: 09/704,176**

**Examiner: Elmore, Stephen C.**

**Filing Date:** 10/31/2000

**Group Art Unit: 2186**

# Title: CACHE COHERENCE PROTOCOL FOR A MULTIPLE BUS MICROPROCESSOR SYSTEM

**COMMISSIONER FOR PATENTS**  
**P.O. Box 1450**  
**Alexandria, VA 22313-1450**

**PETITION TO ACCEPT DRAWINGS**  
**(37 C.F.R. § 1.182)**

3. In accordance with the petition fee set forth in 37 C.F.R. § 1.17(h), please charge to Deposit Account **08-2025** the sum of \$130.00.

Respectfully submitted,

by *A. W. Winfield*

Augustus W. Winfield

Reg. No. 34,046

April 28, 2004

Fort Collins, CO 80528-9599

(970)-898-3142

Best Available Copy

- 2 -